

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE (As Autocomous Institute under Kaketiya University, Warangel)
Opp:Yerragottugutta, Bheemaamar (Y), Hasanparthy (Mandal), WARANGAL-506 015, Telangana State, INDIA. কাজনীয় সাহালিকী ঘূৰ বিস্তান মাধ্যান, খগল - ৭০६, ০৭৭ নিলালা বাদ্য, স্বাস্থা
চাৰজ্ঞিয়া কান্তবিভিন্ন অনুষ্ঠা কান্তব্যক্তিক, ১৮০১ৰ্জনিক্ত

Office of the Controller of Examinations

No. MSE Schedule/B.Tech. I-Sem (URR24)/ 2025/784-2

Date: 08-10-2025

MID SEMESTER EXAMINATIONS (MSE) FOR B.TECH. I-SEMESTER(URR-24), OCTOBER-2025

TIME - TABLE

Examination Timings: FN: 10.00 am to 11.30 am

								Examination Thinings. TN. 10.00 and to 11.50 and				
Date & Day of Examination	STREAM - I				STREAM - II							
	CSE	EEE	ECE	ECIE	CE	ME	IT	CSE(N)	CSE(IOT)	CSE(AIML)	CSE(DS)	
23.10.2025 Thursday	Environmental Studies	Environmental Studies	Environmental Studies	Environmental Studies	Essentials of Civil Engineering	Engineering Graphics & CAD			-			
24.10.2025 Friday	Programming For Problem Solving With C	Programming For Problem Solving With C	Programming For Problem Solving With C	Programming For Problem Solving With C	Programming For Problem Solving With C	Programming For Problem Solving With C	Programming For Problem Solving With C	Programming For Problem Solving With C				
25.10.2025 Saturday	Differential Calculus and Ordinary Differential Equations	Differential Calculus and Ordinary Differential	Differential Calculus and Ordinary Differential	Differential Calculus and Ordinary Differential								
27.10.2025 Monday	Basic Electrical Engineering	Basic Electrical Engineering	Basic Electrical Engineering	Basic Electrical Engineering	English Communication and Report Writing	English Communication and Report Writing	English	English Communication and Report Writing	Equations English Communication and Report Writing	Equations English Communication and Report Writing	Equations English Communication and Report Writing	
28.10.2025 Tuesday	Computer Organization and Architecture	Electrical Measurements & Sensors	Switching Theory and Logic Design	Electronic Measurements and Instrumentation	Engineering Mechanics	Thermo- Dynamics	Digital Logic Design	Digital Logic Design	Digital Logic Design	Switching Theory and Logic Design	Switching Theory and Logic Design	
29.10.2025 Wednesday	Engineering Physics	Engineering Physics	Engineering Physics	Engineering Physics	Engineering Chemistry	Engineering Chemistry	Engineering Chemistry	Engineering Chemistry	Engineering Chemistry	Engineering Chemistry	Engineering Chemistry	

Candidate should occupy his/her seat at least 10 minutes before the commencement of the examination.

To: Exam Branch Notice Boards

Copy to: 1. Principal for favour of information 2.Dean, Academic Affairs 3. HOD's- with a request to display in the Dept Notice Boards & share in the Class WhatsApp groups 4. Training & Placement Officer 5. Web Team- with a request to display on the kitsw.ac.in/examinations/Examinations_schedules.html

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